Laurence Nagel

Chair, Circuit Seed Technical Advisory Board at Circuit Seed

Kensington, CA, US

Electrical Engineering consultant with 45 years of experience in integrated circuit simulation and design.

Biography

Laurence W. Nagel is an independent consultant in the San Francisco Bay Area. He has worked in the integrated circuit industry for almost 50 years. While earning his BS, MS, and PhD degrees at the University of California, he developed the SPICE circuit simulation program which launched a cottage industry of SPICE simulation tools.

Mr. Nagel then began a 20 year career at Bell Laboratories which included developing the ADVICE circuit simulation program; participating in the development of the Kull-Nagel bipolar model; designing analog circuits for submicron NMOS processes; working in the AT&T Intellectual Property Division on assertion of patents and negotiation of patent licenses; and serving as project manager in the development of the Celerity circuit simulation program.

Mr. Nagel then joined Anadigics, Inc., where he managed simulation of RF integrated circuits; modeling and characterization of GaAs MESFET device processes; and importing silicon CMOS design tools and foundry support.

In 1998, Mr. Nagel founded his own company, Omega Enterprises, to consult on analog circuit design, circuit simulation, semiconductor device modeling, and as an expert witness in patent litigation and trade secret misappropriation matters.

Areas of Expertise

Patent and Trade Secret Litigation, Integrated Circuit Processing Technology, Analog Circuit Design, Semiconductor Device Modeling, Circuit Simulation

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Affiliations

IEEE Life Fellow

Education

University of California, Berkeley

MS, EECS

University of California, Berkeley

BS, EECS

University of California, Berkeley
PhD, EECS

Accomplishments

IEEE Donald O. Pederson Award in Solid-State Circuits (2019)
Award "for the development and demonstration of SPICE as a tool to design and optimize electronic circuits".
